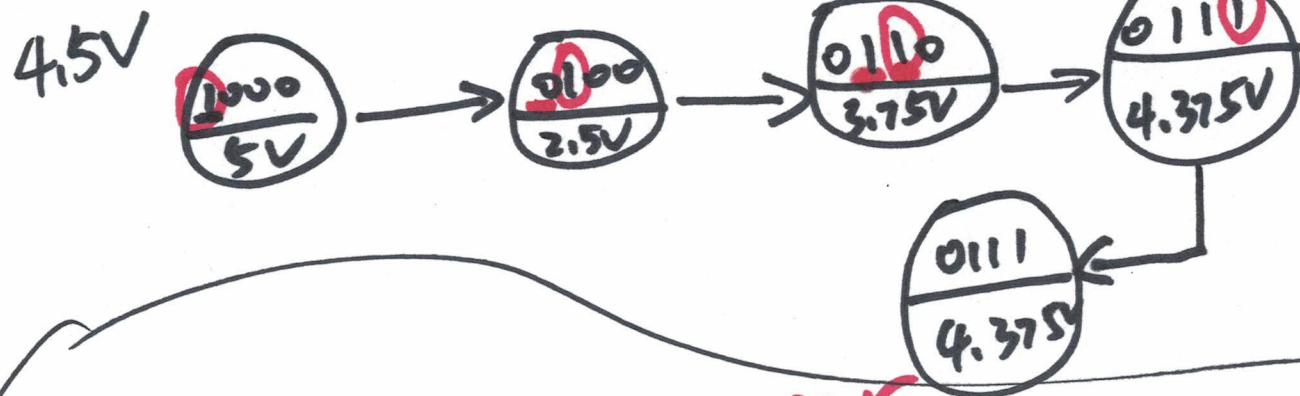
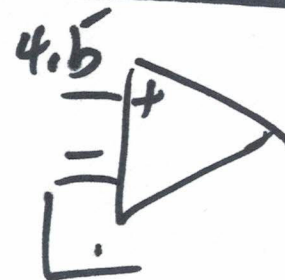
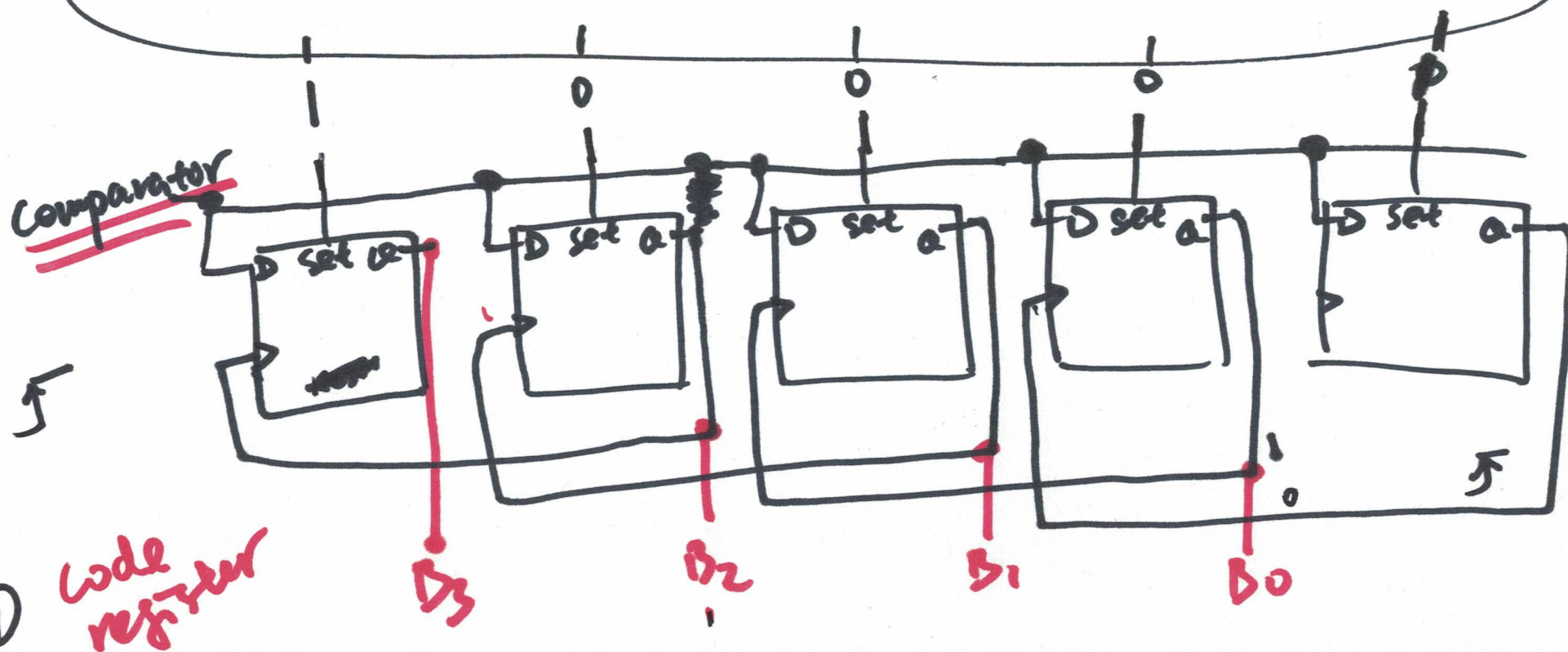


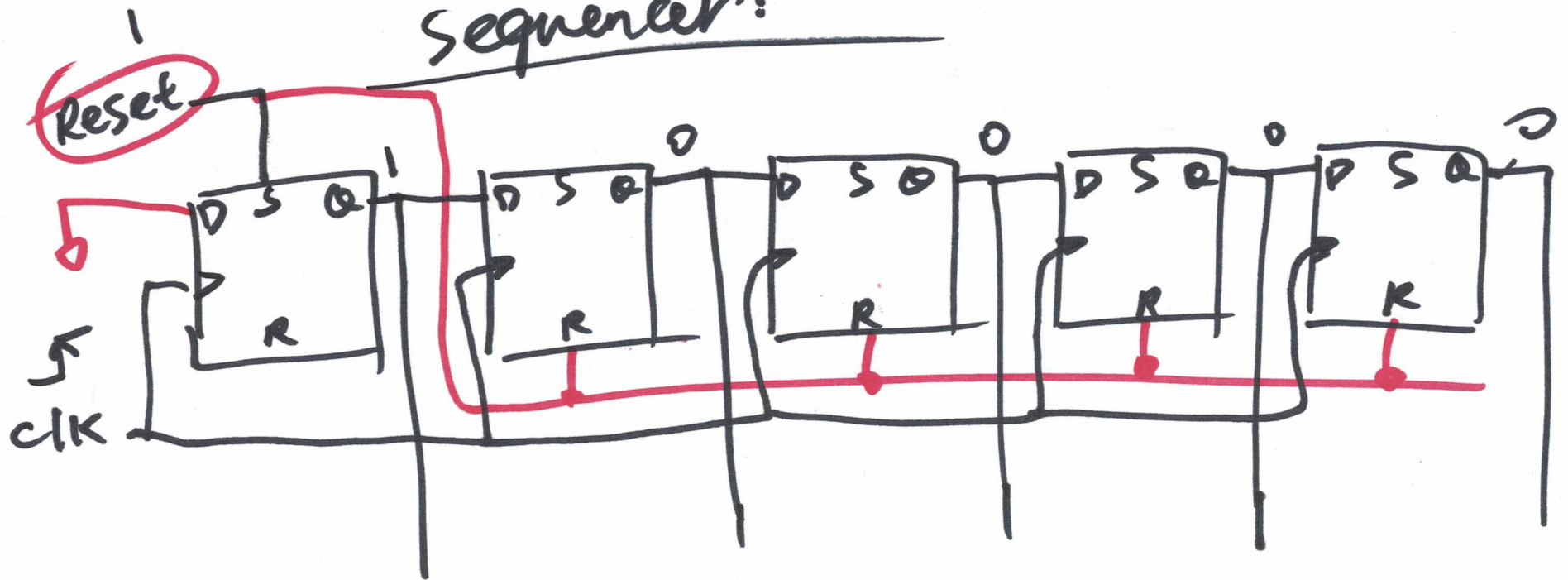
$V_{in} = 4.5V$ $V_{ref} = 12V$ 4-bit SAR ADC



Sequencer

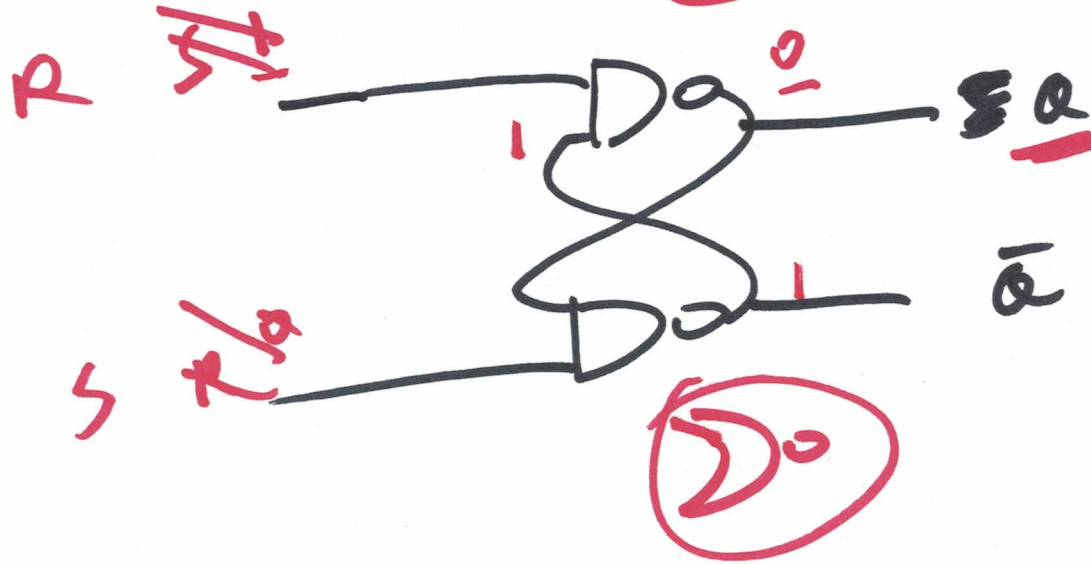


sequencer:



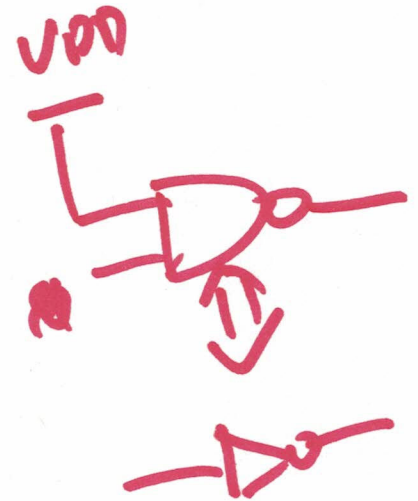
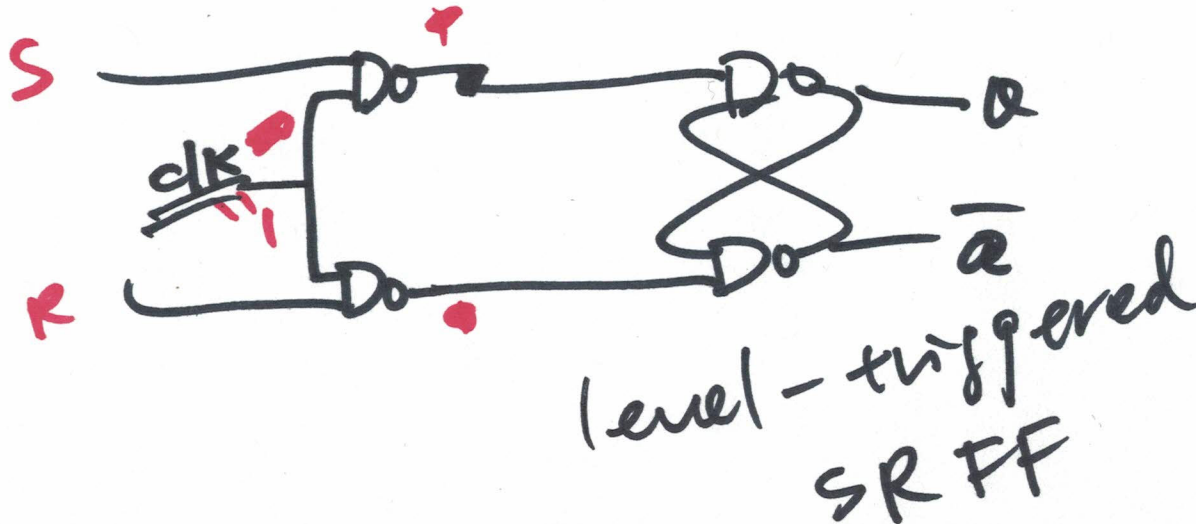
(b)

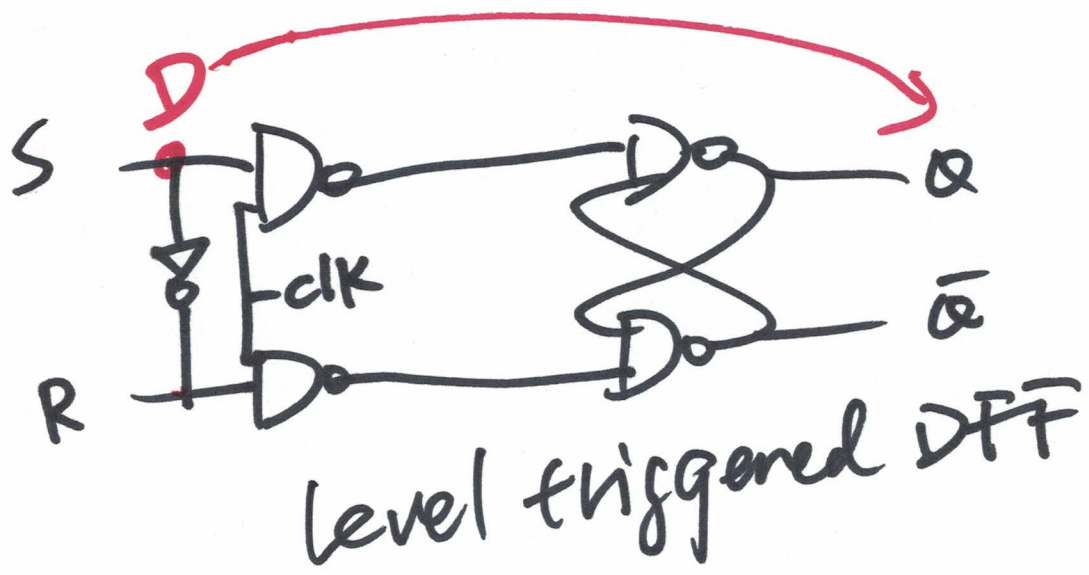
SR Latch



R	S	Q	$\bar{Q}$
0	0	1	1
1	1	memory	

SR Flip Flop





Q