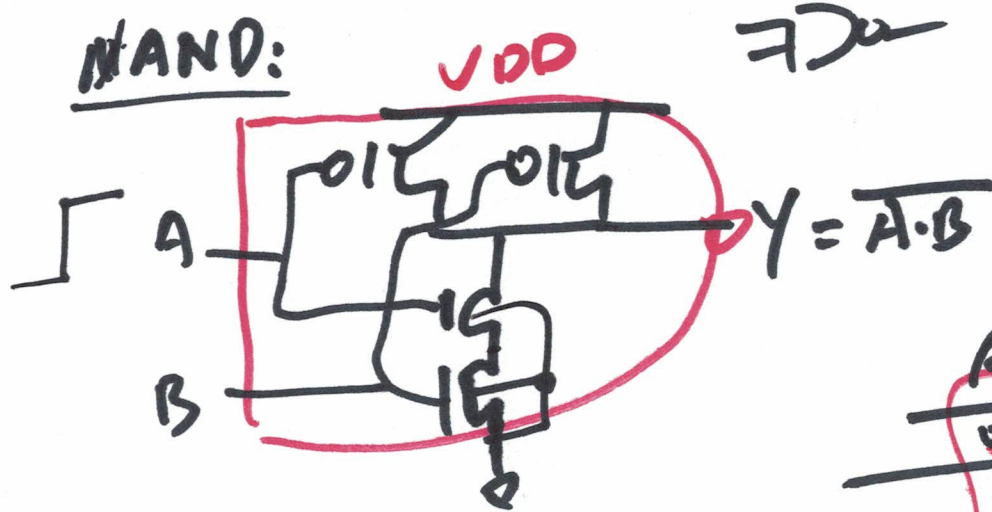


AOI Logic (AND-OR-Inverter)

NOT:



NAND:



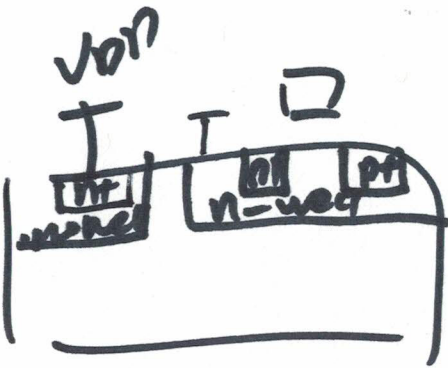
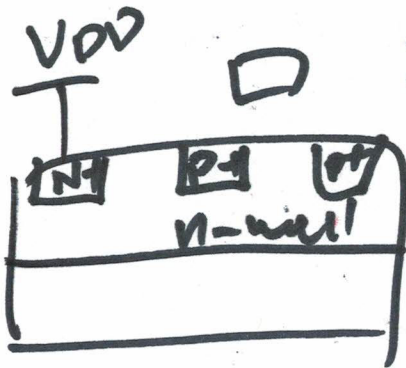
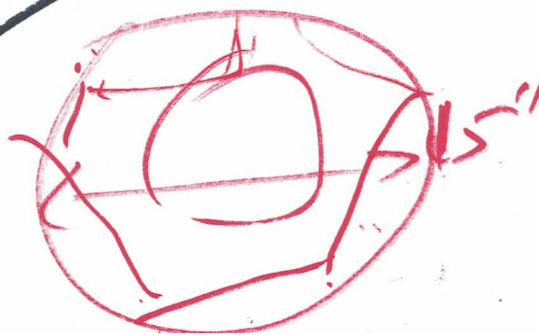
A	B	Y
0	0	1
0	1	1
1	0	1
1	1	0

MUSE:

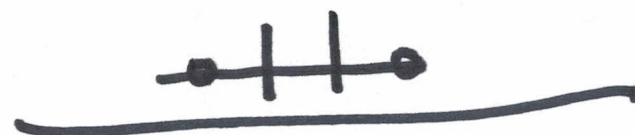
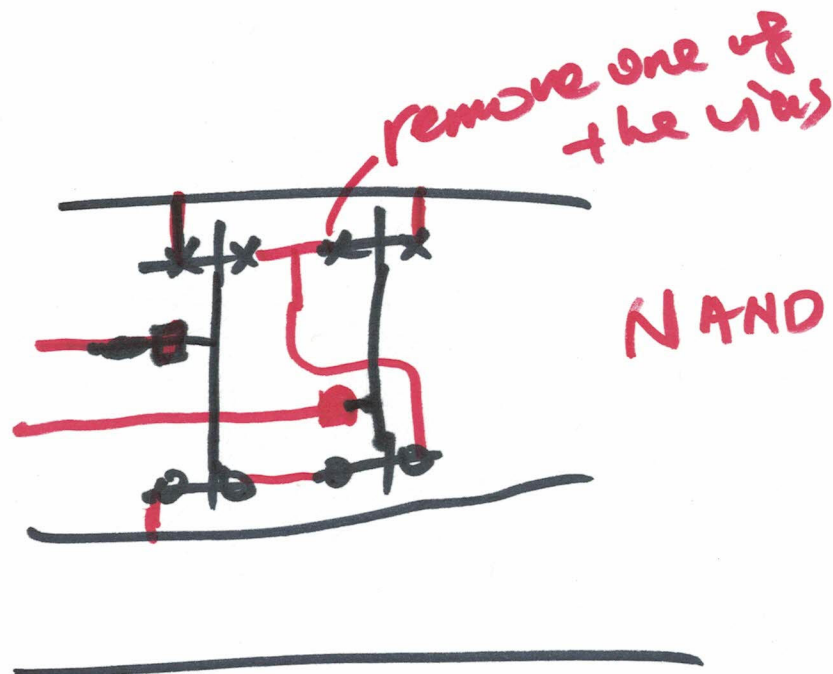
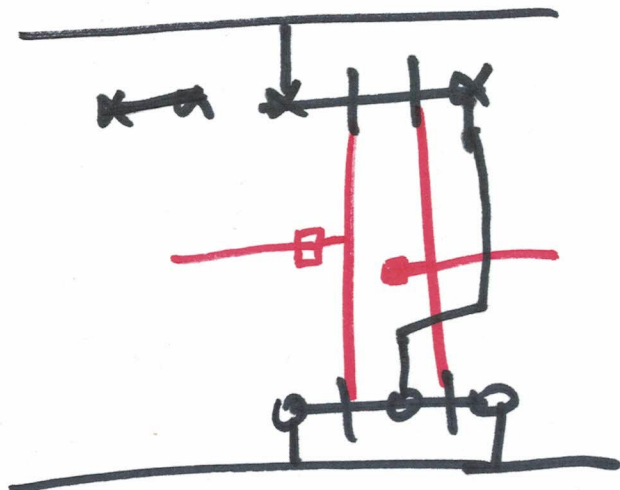
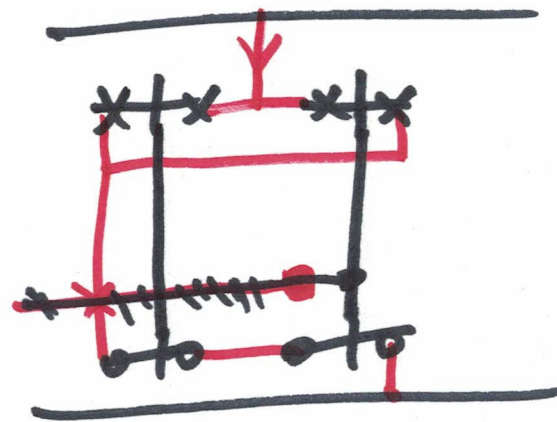
TSMC 180nm $\downarrow$ 6K 95%
 $\downarrow$ 40 90nm 710K
 TSMC 28nm 13K

CMOS

MOSIS
PDK CS MPW



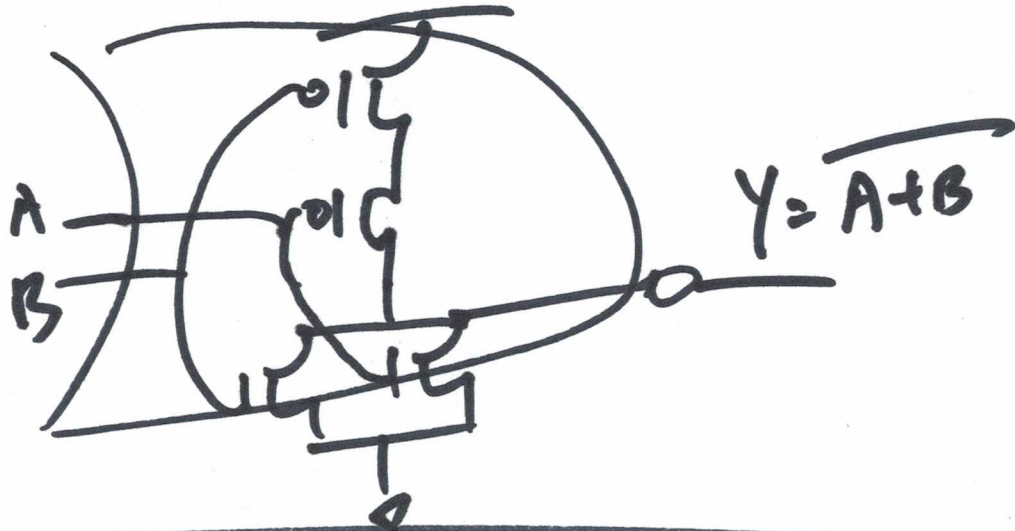
{ PMOS: $\times$ | $\times$
 { NMOS: $\circ$ | $\circ$



②

NOR:

A	B	Y
0	0	1
0	1	0
1	0	0
1	1	0



$$XOR = \bar{A}B + A\bar{B}$$

