

ENGR338\_Tucson:PMOS\_IV{lay}

Components Explorer Layers

LIBRARIES

- ENGR338\_Tucson
  - Ideal\_10Bit\_ADC
  - Ideal\_10Bit\_DAC
  - Ideal\_ADCbit
  - Ideal\_DACbit
  - Ideal\_Differencer\_2
  - Ideal\_Opamp
  - Ideal\_Sample&Hold
  - Ideal\_Switch
  - NMOS\_IV
    - NMOS\_IV{sch}
    - NMOS\_IV{lay}
  - PMOS\_IV
    - PMOS\_IV{sch}
    - PMOS\_IV{lay}
  - R\_2R\_Ladder
  - R\_2R\_Ladder\_v2
  - R\_2R\_Subcell
- spiceparts

ERRORS

- ERC Well Check
- ERC Well Check
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- Layout DRC (full)
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- PMOS\_IV
  - [1] Minimum width/height error((Y axis)): cell 'PMOS\_IV'
  - [2] Notch (layer 'N-Well'): node Metal-1-N-Well-Cd
  - [3] Notch (layer 'N-Well'): node Metal-1-P-Active
- Layout DRC (full) [Current]

JOBS



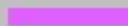

















Electric Messages

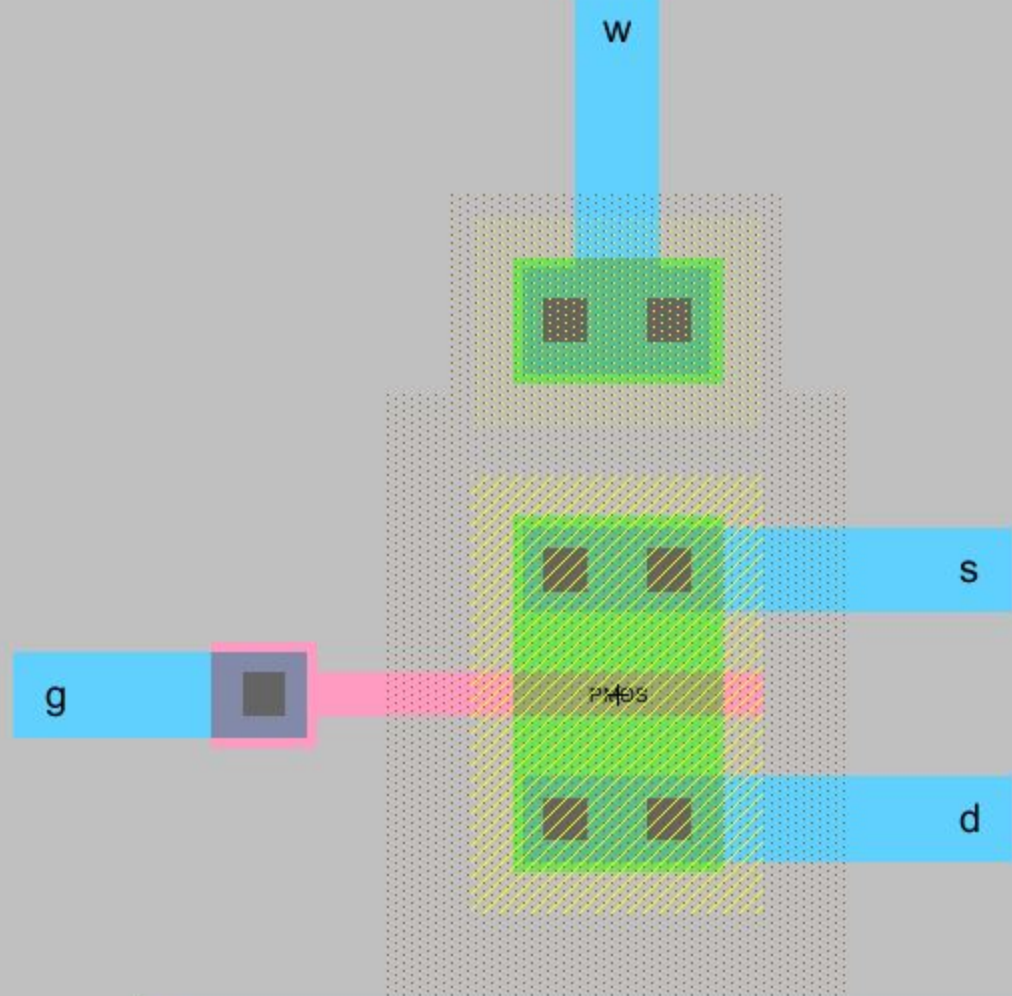
Running DRC with area bit on, extension bit on, Mosis bit on  
Checking again hierarchy  
Layout DRC (full) finished with Errors  
Found 9 networks  
Checking cell 'PMOS\_IV'  
FOUND 3 ERRORS  
3 errors and 0 warnings  
Layout DRC (full) found 3 errors  
Type > and < to step through errors, or open the ERRORS view in the Project Explorer



Layout DRC (full) found 3 errors, 0 warnings!

OK

Pure ▶	Misc. ▶	Cell ▶
		
		
		
		
		
		 nAct
		 pAct

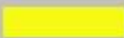

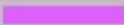














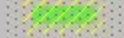




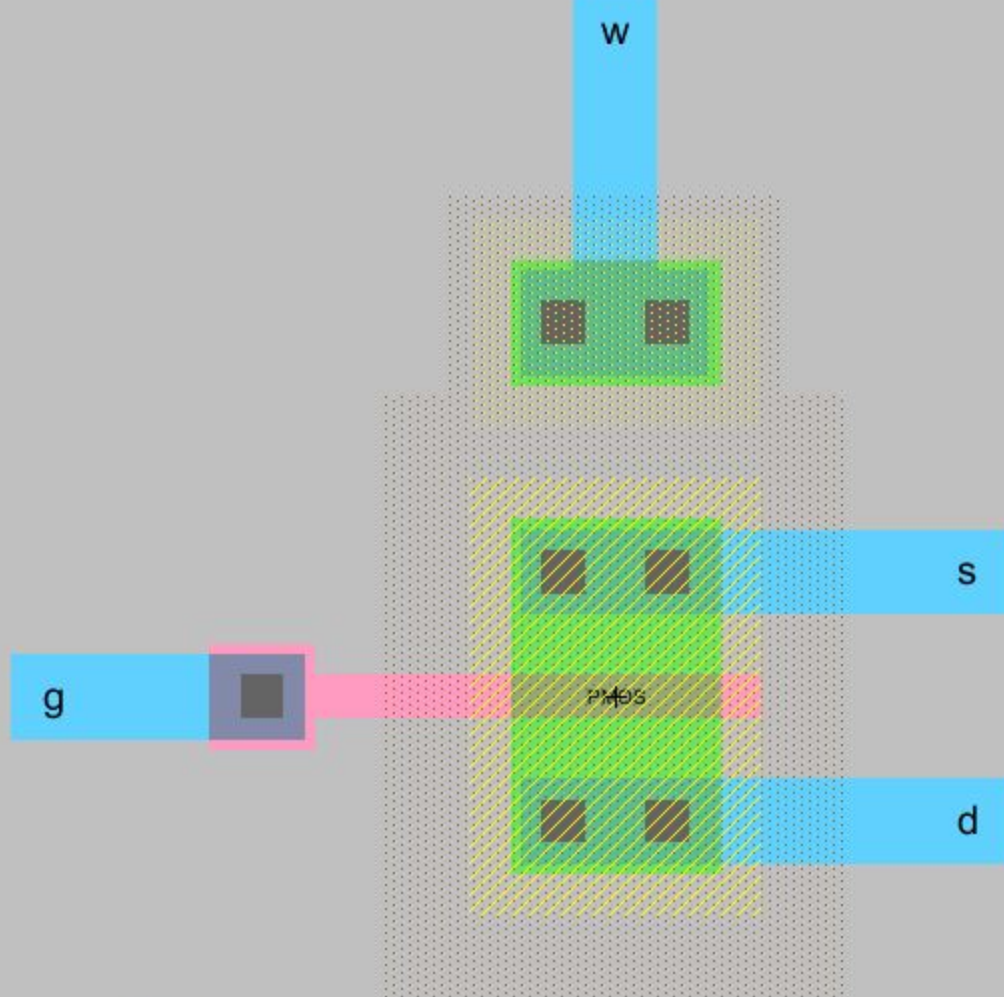
#### Electric Messages

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=====240=====
Checking Wells and Substrates in 'ENGR338_Tucson:PMOS_IV{lay}' ...
  Geometry collection found 6 well pieces, took 0.0 secs
  Geometry analysis used 8 threads and took 0.0 secs
NetValues propagation took 0.0 secs

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Pure	Misc.	Cell
		
		
		
		
		
		nAct 
		pAct 



#### Electric Messages

Moved text (4,1)

-----239-----

Running DRC with area bit on, extension bit on, Mosis bit

Checking again hierarchy .... (0.0 secs)

Found 6 networks