

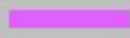
Components Explorer Layers

mocmos

Pure

Misc.

Cell



nAct

pAct

Electric Messages

Moved node Metal-1-N-Active-Con[contact@0] by (0,-1)

-----91-----

Running DRC with area bit on, extension bit on, Mosis bit

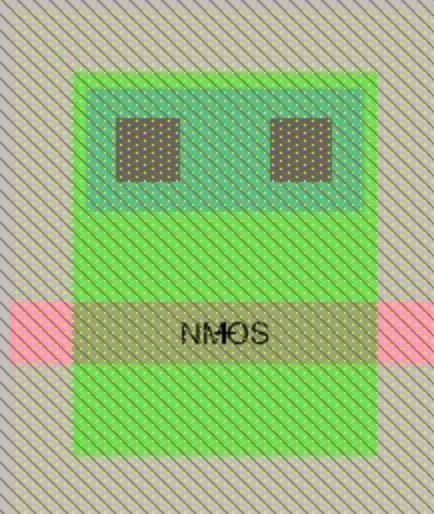
Checking again hierarchy (0.01 secs)

Found 8 networks

Checking cell 'NMOS_IV{lay}'

No errors/warnings found

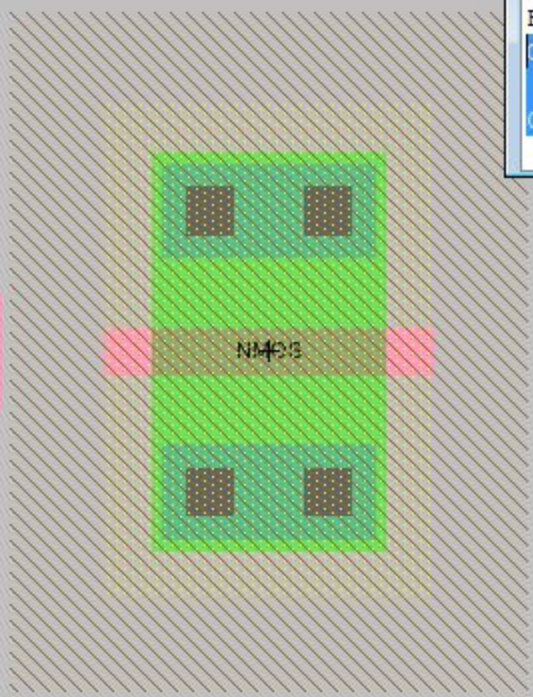
0 errors and 0 warnings found (took 0.03 secs)



Components Explorer Layers


















LIBRARIES

- ENGR338_Tucson
 - Ideal_10Bit_ADC
 - Ideal_10Bit_DAC
 - Ideal_ADCbit
 - Ideal_DACbit
 - Ideal_Differencer_2
 - Ideal_Opamp
 - Ideal_Sample&Hold
 - Ideal_Switch
 - NMOS_IV
 - NMOS_IV{sch}
 - NMOS_IV{lay}
 - PMOS_IV
 - PMOS_IV{sch}
 - PMOS_IV{lay}
 - R_2R_Ladder
 - R_2R_Ladder_v2
 - R_2R_Subcell
- spiceparts
- ERRORS
- JOBS



Electric Messages

```
Moved node Metal-1-N-Active-Con[contact@1] by (0,1
=====124=====
Running DRC with area bit on, extension bit on, Mo
Checking again hierarchy .... (0.0 secs)
Found 7 networks
Checking cell 'NMOS_IV{lay}'
    No errors/warnings found
0 errors and 0 warnings found (took 0.01 secs)
```

-  Ideal_10Bit_ADC
-  Ideal_10Bit_DAC
-  Ideal_ADCbit
-  Ideal_DACbit
-  Ideal_Differencer_2
-  Ideal_Opamp
-  Ideal_Sample&Hold
-  Ideal_Switch
-  NMOS_IV
 -  NMOS_IV{sch}
 -  NMOS_IV{lay}
-  PMOS_IV
 -  PMOS_IV{sch}
 -  PMOS_IV{lay}
-  R_2R_Ladder
-  R_2R_Ladder_v2
-  R_2R_Subcell

spiceparts

ERRORS

JOBS

Wiring added: 1 Polysilicon-1 arc

=====131=====

Running DRC with area bit on, extension bit on, M



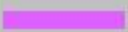














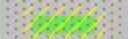




```
Checking again hierarchy .... (0.0 secs)
```

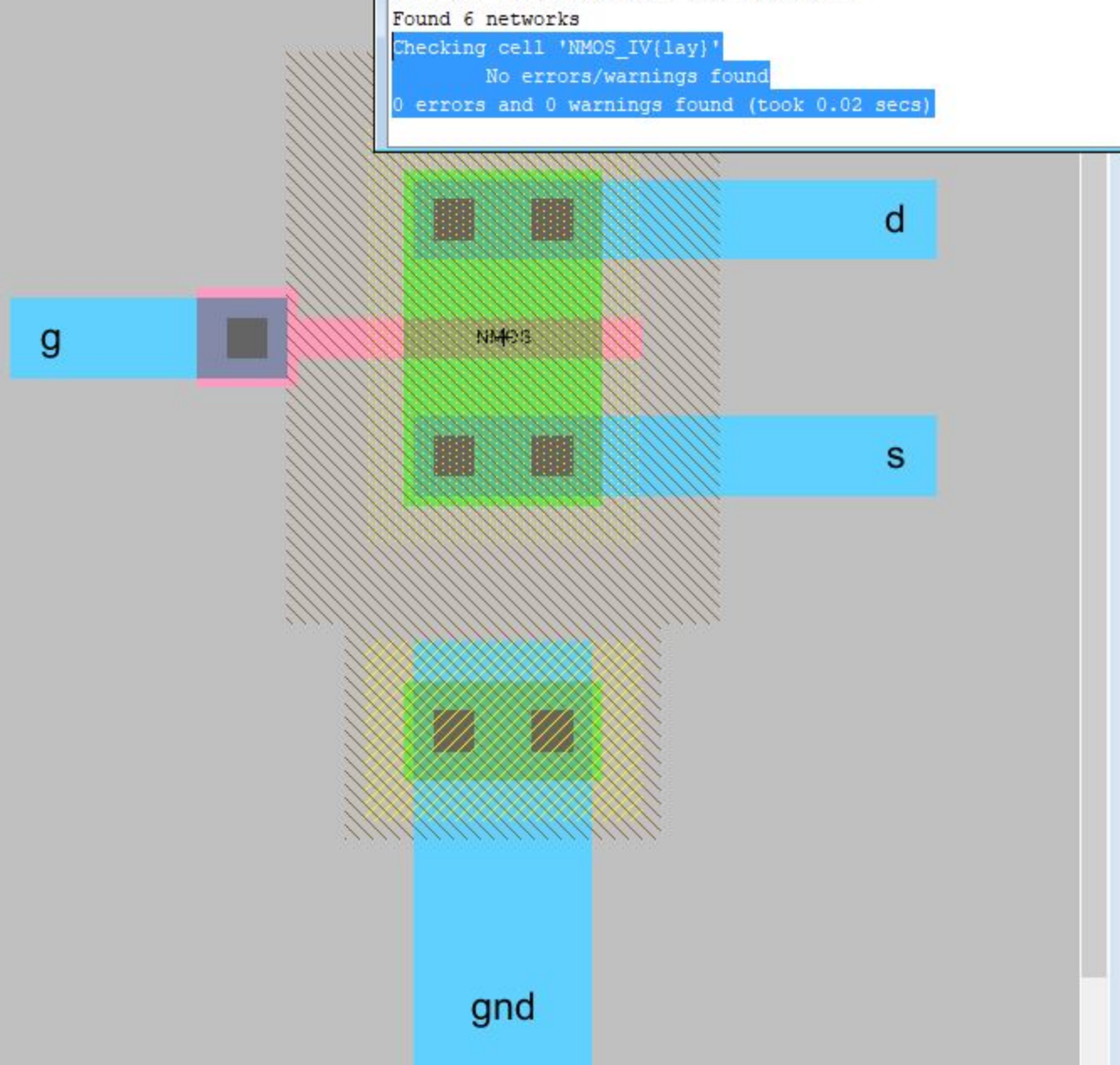
Found 6 networks

```
Checking cell 'NMOS IV{lay}'
```

```
No errors/warnings found
```

```
0 errors and 0 warnings found (took 0.02 secs)
```

Pure	Misc.	Cell
		
		
		
		
		
		nAct 
		pAct 
	pWell 	nWell 



Components Explorer Layers

LIBRARIES

- ENGR338_Tucson
 - Ideal_10Bit_ADC
 - Ideal_10Bit_DAC
 - Ideal_ADCbit
 - Ideal_DACbit
 - Ideal_Differencer_2
 - Ideal_Opamp
 - Ideal_Sample&Hold
 - Ideal_Switch
 - NMOS_IV
 - NMOS_IV{sch}
 - NMOS_IV{lay}
 - PMOS_IV
 - PMOS_IV{sch}
 - PMOS_IV{lay}
 - R_2R_Ladder
 - R_2R_Ladder_v2
 - R_2R_Subcell
- spiceparts
- ERRORS
- JOBS

Electric Messages

Geometry collection found 5 well pieces, took 0.0 secs
Geometry analysis used 8 threads and took 0.01 secs
NetValues propagation took 0.0 secs
Checking short circuits in 1 well contacts
Additional analysis took 0.0 secs
FOUND 2 WELL ERRORS (took 0.01 secs)
ERC Well Check found 2 errors, 0 warnings!
Type > and < to step through errors, or open the ERRORS view in the explorer

ERC Well Check finished with Errors



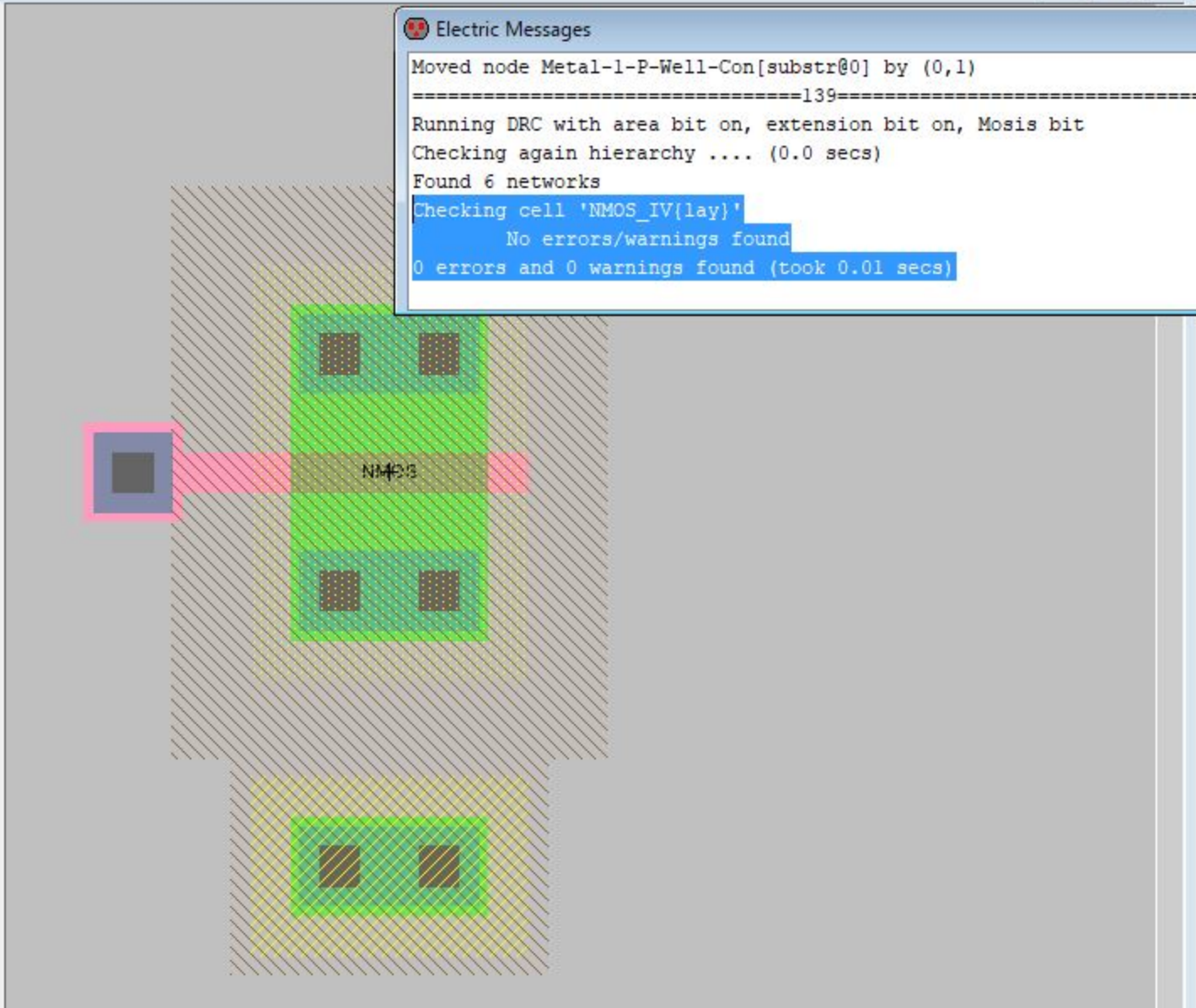
ERC Well Check found 2 errors, 0 warnings!

OK

Components Explorer Layers

LIBRARIES

- ENGR338_Tucson
 - Ideal_10Bit_ADC
 - Ideal_10Bit_DAC
 - Ideal_ADCbit
 - Ideal_DACbit
 - Ideal_Differencer_2
 - Ideal_Opamp
 - Ideal_Sample&Hold
 - Ideal_Switch
 - NMOS_IV
 - NMOS_IV{sch}
 - NMOS_IV{lay}
 - PMOS_IV
 - PMOS_IV{sch}
 - PMOS_IV{lay}
 - R_2R_Ladder
 - R_2R_Ladder_v2
 - R_2R_Subcell
- spiceparts
- ERRORS
- JOBS



Components Explorer Layers

LIBRARIES

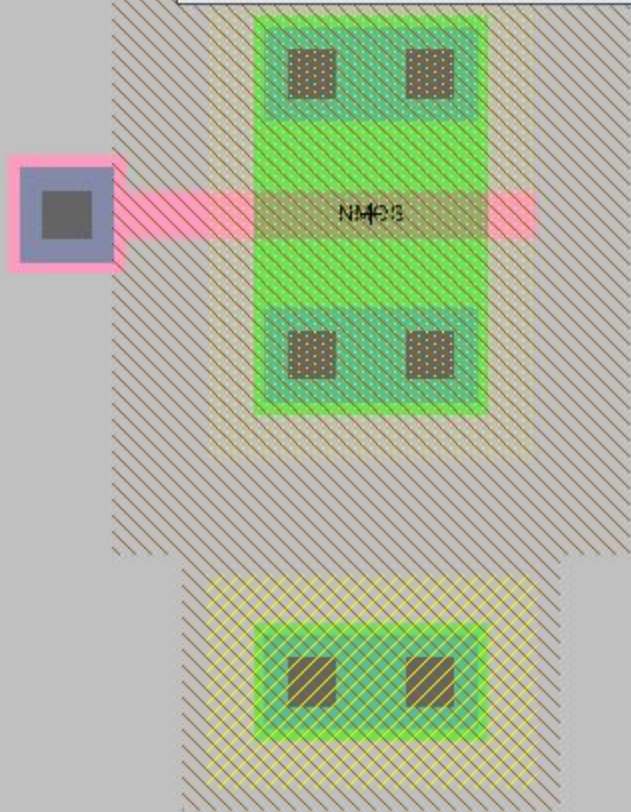
ENGR338_Tucson

- ⊕ Ideal_10Bit_ADC
- ⊕ Ideal_10Bit_DAC
- ⊕ Ideal_ADCbit
- ⊕ Ideal_DACbit
- ⊕ Ideal_Differencer_2
- ⊕ Ideal_Opamp
- ⊕ Ideal_Sample&Hold
- ⊕ Ideal_Switch
- ⊖ NMOS_IV
 - NMOS_IV{sch}
 - NMOS_IV{lay}
- ⊖ PMOS_IV
 - PMOS_IV{sch}
 - PMOS_IV{lay}
- ⊕ R_2R_Ladder
- ⊕ R_2R_Ladder_v2
- ⊕ R_2R_Subcell

spiceparts

⊕ ! ERRORS

⚒ JOBS



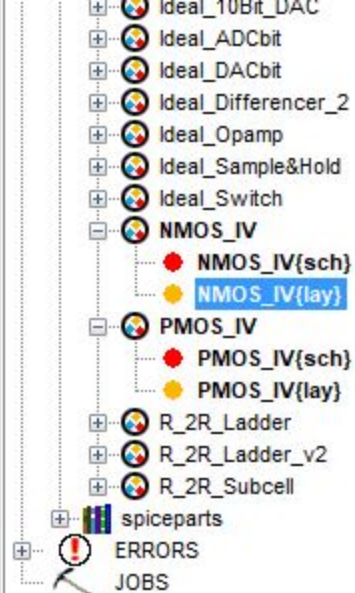
Electric Messages

Geometry collection found 5 well pieces, took 0.0 secs
Geometry analysis used 8 threads and took 0.0 secs
NetValues propagation took 0.0 secs
Checking short circuits in 1 well contacts
Additional analysis took 0.0 secs
FOUND 1 WELL ERRORS (took 0.01 secs)
ERC Well Check found 1 errors, 0 warnings!
Type > and < to step through errors, or open the ERRORS view in the explorer

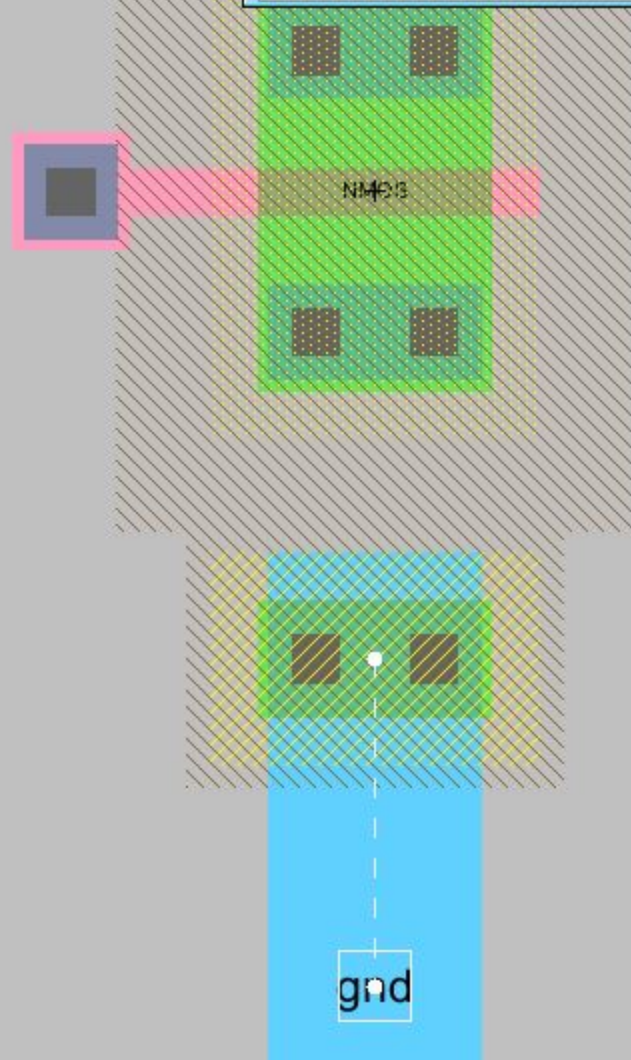
ERC Well Check finished with Errors






















ERC Well Check found 1 errors, 0 warnings!

OK



Geometry analysis used 8 threads and took 0.0 secs
NetValues propagation took 0.0 secs
Checking short circuits in 1 well contacts
Additional analysis took 0.0 secs
No Well errors found (took 0.0 secs)



Pure ▶	Misc. ▶	Cell ▶
		
		
		
		
		
		nAct 
		pAct 
	pWell 	nWell 