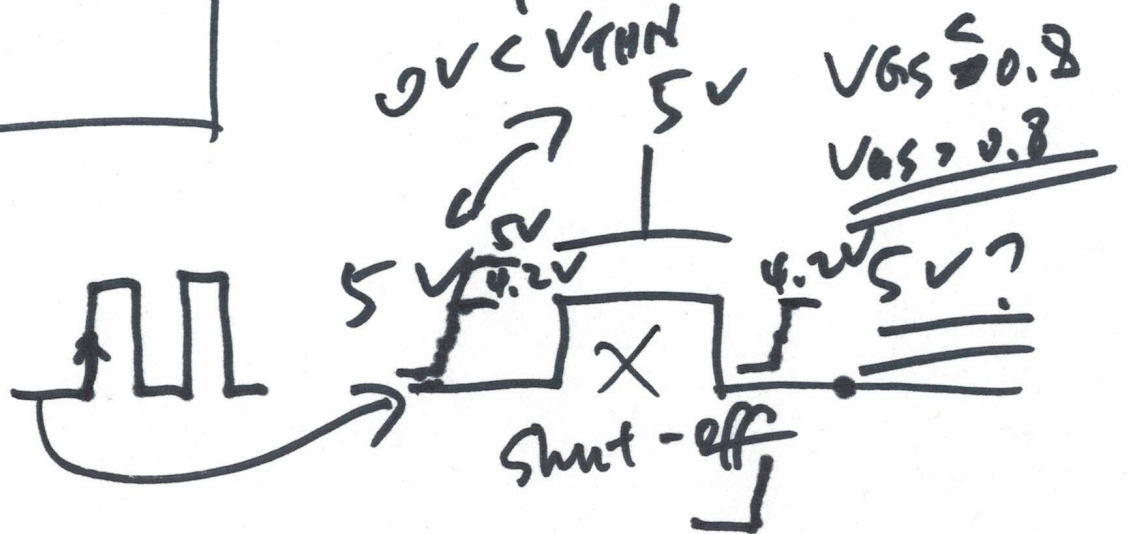
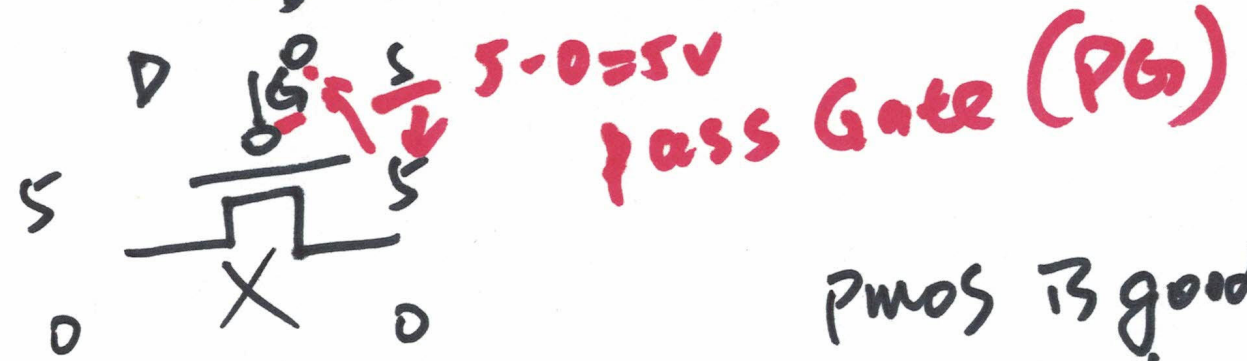
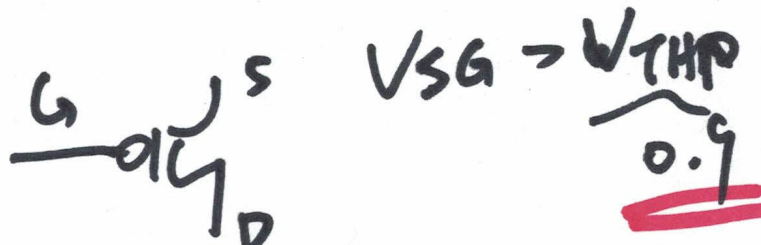
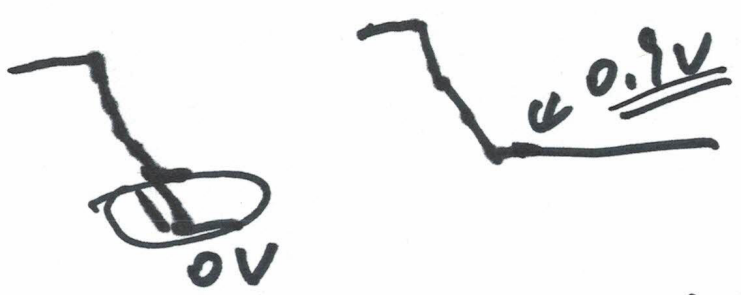


pass-Gate



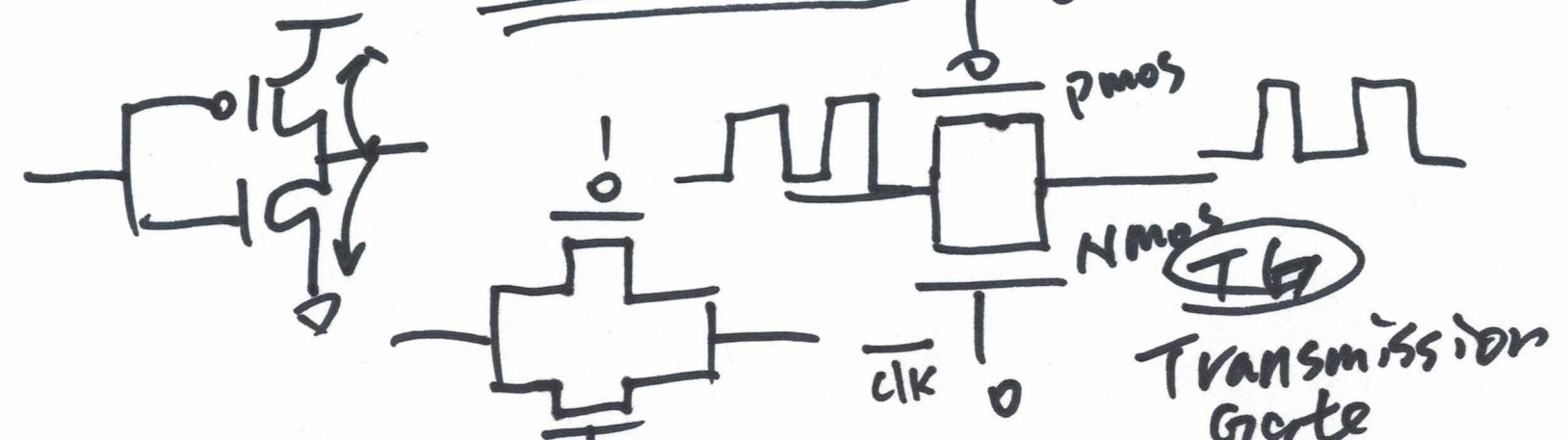


PMOS is good at passing 1's but not 0's.



CMOS logic

~~NMOS logic~~



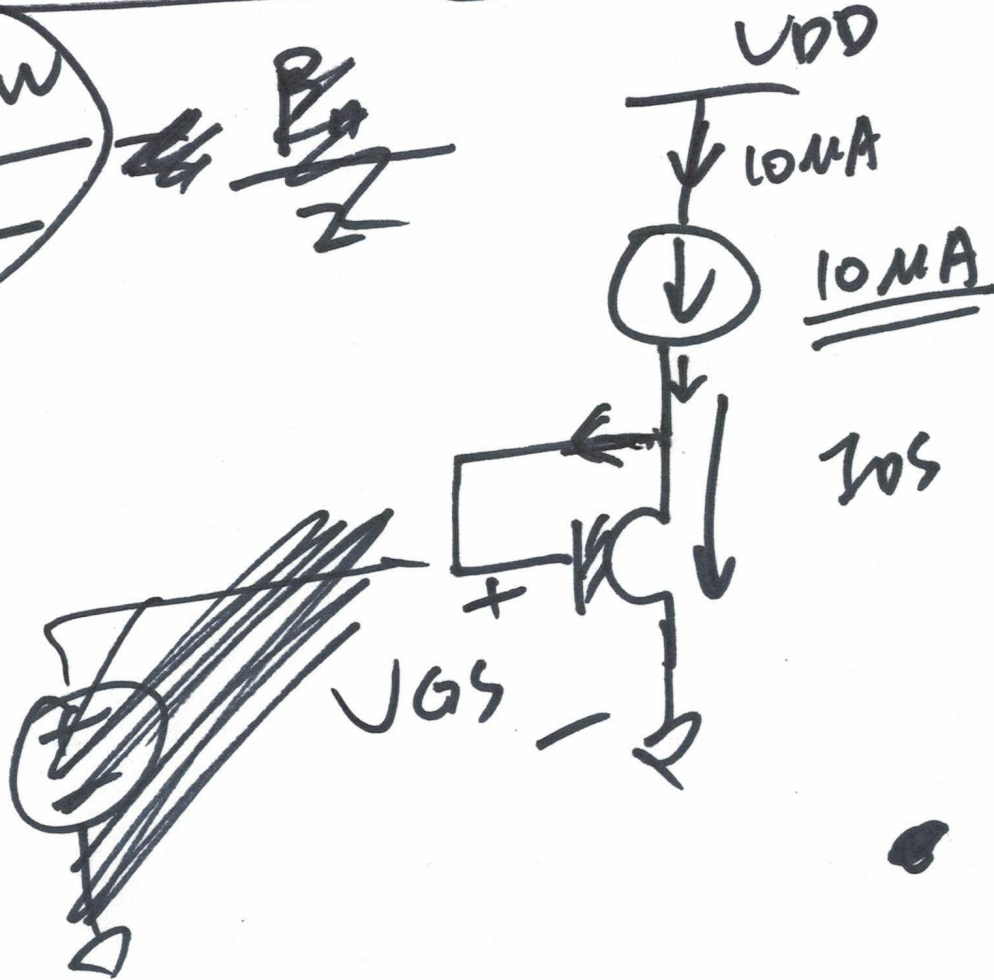
2

Square Law Equation

$$I_{DS} = \frac{K_{Pn} W}{2L} (V_{GS} - V_{thn})^2 = \frac{\beta_n}{2} (V_{GS} - V_{thn})^2$$

$$\beta_n = \frac{K_{Pn} W}{L}$$

$$\frac{\beta_n}{2}$$



→ Bias current
operating
Point