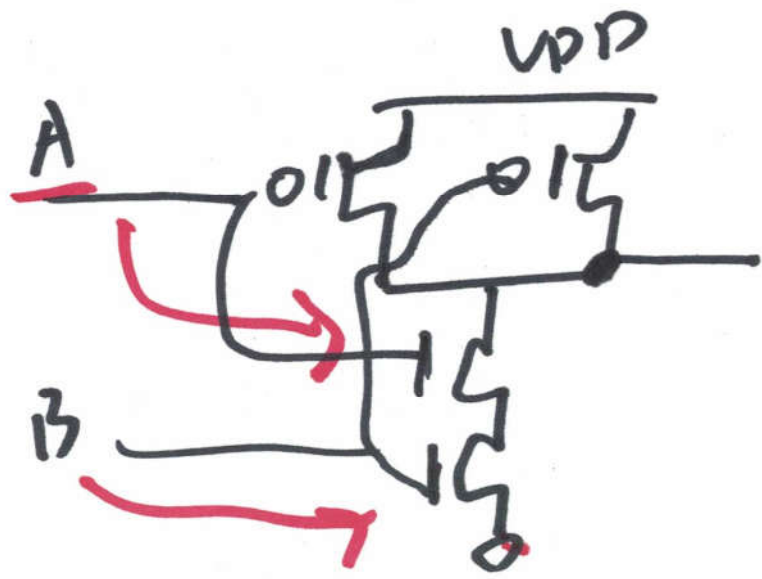
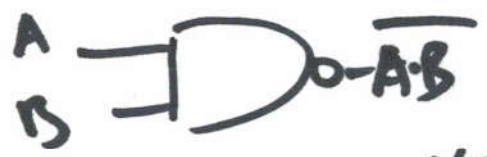


NAND

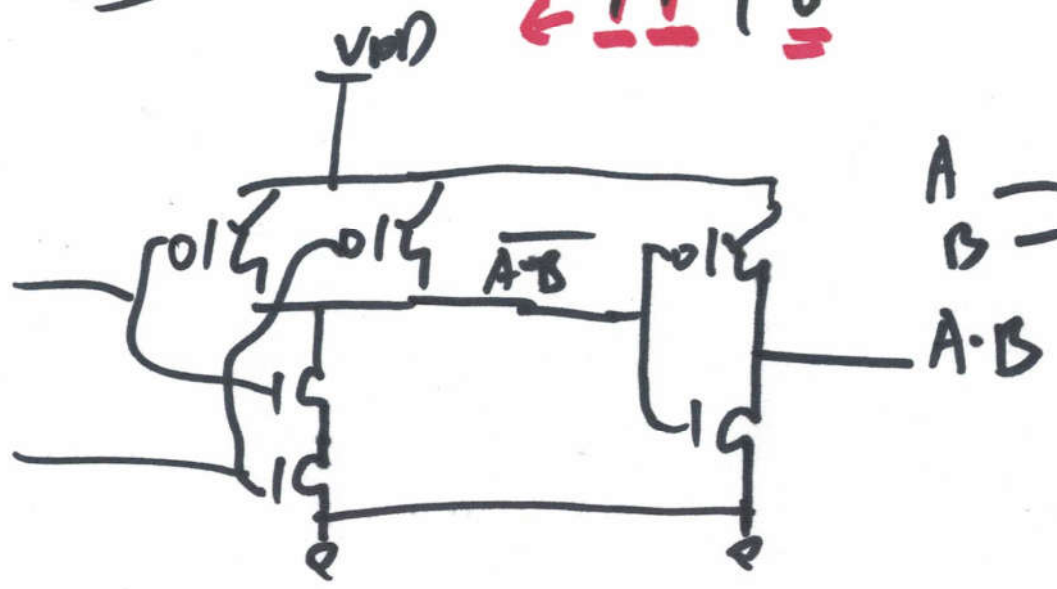


$$\overline{A \cdot B}$$

- 01 $\bar{1}$ good at passing 1's
 - $\bar{1}\bar{1}$ good at passing 0's



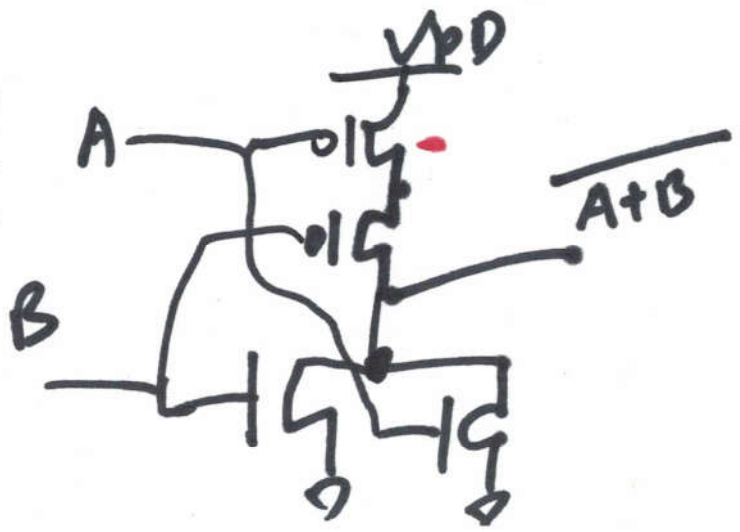
A	B	$Y = \overline{A \cdot B}$
0	0	1
0	1	1
1	0	1
1	1	0



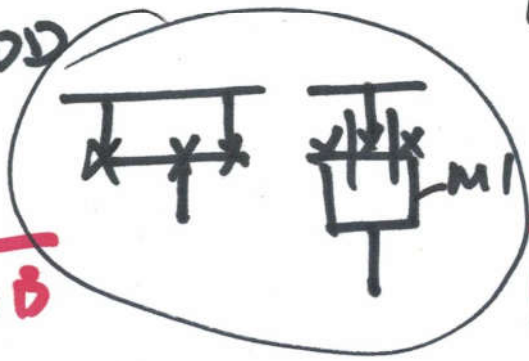
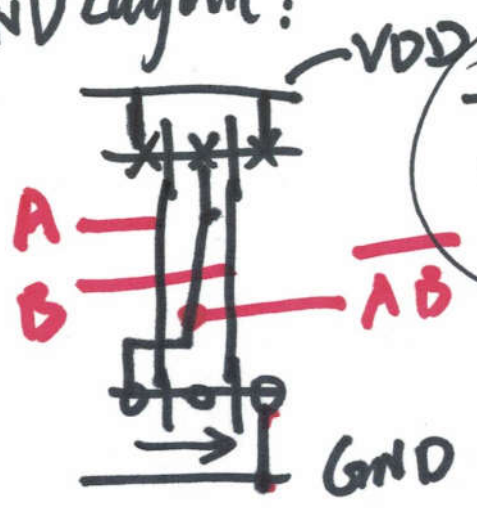
NOR Gate

A	B	$Y = \overline{A+B}$
0	0	1
0	1	0
1	0	0
1	1	0

←



NAND layout:



NOR Gate

